

CSCE 6730: Advanced VLSI Systems

Assignment # 1, Total Points = 100.

Assigned Date: 09 Sep 2010 (Thu), Due Date: 16 Sep 2010 (Thu)

Instructor: Dr. Saraju P. Mohanty

1. Get the following paper from IEEE Xplore Digital Library:

S. P. Mohanty, N. Ranganathan, and R. K. Namballa, "A VLSI Architecture for Visible Watermarking in a Secure Still Digital Camera (S²DC) Design", *IEEE Transactions on Very Large Scale Integration Systems (TVLSI)*, Vol. 13, No. 8, August 2005, pp. 1002-1012.

Read the article and write a 1-page review of it in your own words.