

CSCE 6730: Advanced VLSI Systems

Assignment # 3, Total Marks = 100.

Assigned Date: 22nd Oct 2009 (Thu), Due Date: 29th Oct 2009 (Thu)

Instructor: Dr. Saraju P. Mohanty

1. Design a 6 transistor SRAM using 32nm technology in LTspice. Perform simulations to prove its different operations. Calculate its power dissipation.