

# CSCE 6730: Advanced VLSI Systems

Assignment # 2, Total Marks = 100.

Assigned Date: 08<sup>th</sup> Oct 2009 (Thu), Due Date: 15<sup>th</sup> Oct 2009 (Thu)

Instructor: Dr. Saraju P. Mohanty

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1. Consider design of a 45nm inverter circuit in LTspice. Perform simulations to characterize it for all possible sources of power and leakage dissipation.