

# CSCE 6730: Advanced VLSI Systems

Assignment # 1, Total Marks =  $4*25 = 100$ .

Assigned Date: 22<sup>nd</sup> Sep 2009 (Tue), Due Date: 29<sup>th</sup> Sep 2009 (Tue)

Instructor: Dr. Saraju P. Mohanty

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1. List three free and/or commercial high-level synthesis tools. Explain the capabilities of each one of these tools.
2. Explain the different phases of high-level synthesis.
3. Discuss 3 important issues for nano-CMOS based design.
4. With an example discuss difference between behavioral and structural description of a circuit.