

CSCE 5730: Digital CMOS VLSI Design

Assignment # 1, Total Marks = $5 \times 20 = 100$.

Assigned Date: 17th Sep 2008 (Wed), Due Date: 29th Sep 2008 (Mon)

Instructor: Dr. Saraju P. Mohanty

1. Give a list of five journals in the area of VLSI, VLSI design automation. Provide their publishers name and also their homepage URL.
2. Give a list of five leading conferences in the area of VLSI, VLSI design automation. Provide their homepage URL and last two years venue.
3. Give a list of five free and/or commercial CAD tools.
4. Assume a wafer size of 14inches, a die size of 3.5cm^2 , $1.5\text{defects}/\text{cm}^2$, and $\alpha=3$. Determine the die yield of this CMOS process run.
5. Consider design of an MPEG chip. Explain different levels of digital design abstraction through this chip.