

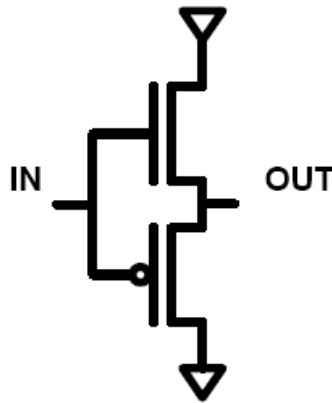
CSCE 5730: Digital CMOS VLSI Design

Assignment 4, Dated: 22nd February 2006 (Wed)

Instructor: Dr. Saraju P. Mohanty

Due Date: 1st March 2006 (Wed)

1. Determine the output voltage for the following circuit when a constant supply voltage of V volts applied as the input and simulate it using DSCH as per the discussions in the class. Comment on the operation of the circuit.



2. Build and simulate an 8-bit register using DSCH.
3. Build and simulate an 8-bit adder using DSCH.