

CSCI 5330: Digital CMOS VLSI Design

Assignment 7, Dated: 22nd Mar 2005 (Tue), Due Date: 5th Apr 2005 (Tue)

Instructor: Dr. Saraju P. Mohanty

1. Problem 10.12 (page-711). Simulate the design using DSCH/Microwind.
2. Using DSCH/Microwind simulate the array funnel shifter in Fig 10.64 (page-692).
3. Using DSCH/Microwind simulate the array multiplier in Fig 10.70 (page-695).