

## **CSCI 5330: Digital CMOS VLSI Design**

**Assignment 2, Dated: 03<sup>rd</sup> Feb 2005 (Thu), Instructor: Dr. Saraju P. Mohanty**  
**Due Date: 10<sup>th</sup> Feb 2005 (Thu)**

---

Read the slides 47-72 discussed in 03<sup>rd</sup> Feb 05 lecture that describes NOR design using microwind tool. And do all the steps and submit final design report and simulation results. The reports should be prepared as per the guidelines discussed in the first lecture.