

Lab 5 (14 Oct 2004): CSCI 4330 /5330: Digital Systems Design with VHDL
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1. An array multiplier for unsigned numbers is explained in section 5.6.1 (page-291) of the text book. You need to design a 4-bit circuit using either behavioral or structural VHDL. Then, simulate it either writing VHDL test bench or using Altera waveform editor.
2. Design and simulate individual 4-bit circuits performing the following functions:
 - i. OR
 - ii. AND
 - iii. NOT
 - iv. NAND
 - v. NOR
 - vi. XOR
 - vii. NOT