

Lab 10 (18 Nov 04) CSCI 4330/5330: Digital Systems Design with VHDL
Instructor: Saraju P. Mohanty

NOTE: Show all logic minimization process/steps in your report.

1. 6.22 (page-375)
2. 6.28 (page-375)
3. Write a procedure that calculates the sum of squares of elements of an array variable deviations. The elements are real numbers. Your procedure should store the result in a real variable sum_of_squares.