# **Energy-Efficient Physical Unclonable Functions for Secure IoT Environment**

### Abstract

- The Internet of things is currently the most sought after solution for many day-to-day issues that we are facing.
- This work presents an energy efficient security solution, the PUF, for making the Internet of Things a safer environment.
- Low power consumption and low chip area makes it easier to be deployed anywhere.



- In

### Fig. 1 IoT Security

### **Simulation Results of The Design**

- Uniqueness: The same key should not be obtained using any other PUF design. Uniqueness is calculated using Hamming Distance.
- Average Power: Average Power consumed by the entire circuit.
- Reliability: The PUF module should give the same key. Even the environmental effects should not change the key.
- Security: The module should be resistant to different attacks on the circuit.
- these results are • Each of presented below.

| Parameter  | Value    |
|--|----------|
| Conventional Ring Oscillator Physical<br>Unclonable Function       |          |
| Average Power  | 310.8 μW |
| Hamming Distance   | 50%      |
| Time to generate key   | 150 ns   |
| Proposed Hybrid Oscillator Arbiter Physical<br>Unclonable Function |          |
| Average Power  | 123.8 μW |
| Hamming Distance   | 48.1%    |
| Time to generate key   | 150 ns   |

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### **Engineering Problem Overview**

IoT environment an everyTHING is connected and where 'everything' is connected, security is one of the main concerns.

• If an environment is attacked and breached, an entire city or home can be in chaos.

• One of the efficient solutions for this issue is hardware security.

 Physical Unclonable Functions (PUF) take advantage of the manufacturing variations in an IC. • A PUF key is never stored in memory which makes it more secure and robust.

• The input to a PUF is Challenge Input (in form of binary) and the output is Response (also binary).

- flipflop.







### **Design of Proposed Physical Unclonable Function and Deployment in Device**

• During the fabrication, due to various factors, variations will be introduced into the devices on the IC. • These variations will affect the output of the devices and no two devices will give an identical output. • In PUF design, Ring Oscillators generate oscillations but due to manufacturing variations, no two frequencies are the same.

• Multiplexers select a pair of ROs and give signals to the D-Flipflop. The PUF key is generated from the

• To generate an *N*-bit key, 2N ring oscillators are needed in this design.

Fig. 2. Hybrid Oscillator Arbiter Physical Unclonable Function



Fig. 3. Deployment of PUF in Iot Device