



UNIVERSITY OF NORTH TEXAS

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CSE

COMPUTER SCIENCE & ENGINEERING STUDENT NEWSLETTER

January 2015 Edition

Department of Computer Science and Engineering News

Welcome New CSE Faculty Member

CSE Distinguished Speakers in Spring 2015

Welcome First CSE Grad Track Students

UNT REU renewed for 2015-2017

CSE sponsoring Workshop on Hot Topics in Networking and Security

The HiLT Lab hosts NACLO 2015

CERL (Computational Epidemiology Research Laboratory) News

News from Computer Systems Research Laboratory

News from INformation Security and Privacy: Interdisciplinary Research and Education (INSPIRE) Lab

Human Intelligence and Language Technologies Lab News

Professor Mohanty Chairs International Conference and Honored as a Distinguished Alumnus

Student News

Congratulations to CSE graduates

CSE Graduate Student wins Spirit of Innovation Competition

CSE students participate in NASA Design Challenge

Graduate Exhibition on March 7

CSE Students invited to join ACMW Book Club

College of Engineering News

Engineers Week and Career Fair coming up

College of Engineering Tutoring Center

College of Engineering offers Undergraduate Student Professional Development Fund

Greetings from the CSE Chair

Dear CSE Students,

Happy New Year and welcome to the UNT Department of Computer Science and Engineering for the Spring 2015 semester! Please join me in welcoming our new faculty member, Dr. Robin Pottathuparambil. You may find out more about him below. At the same time we bid farewell and best wishes to departing faculty Dr. Qunfeng Dong and Dr. Mahadevan Gomathisankaran. We are recruiting this semester for a new faculty member in the computer systems area and will be looking to replace Drs. Dong and Gomathisankaran as quickly as possible.

If you are graduating next year with an outstanding academic record, I encourage you to consider the Grad Track for undergraduate students in Computer Science and Computer Engineering programs. Students who are admitted to Grad Track can enroll in up to nine

Professor Mohanty Chairs International Conference and Honored as a Distinguished Alumnus



Professor Mohanty welcoming the audience in the inaugural speech.



Professor Mohanty receives a distinguished alumni award from the Principal (i.e. Dean) of the Government College of Engineering & Technology, Bhubaneswar.

Professor Saraju Mohanty was a general chair for 13th International conference on information held in Bhubaneswar, India during December 22-24, 2014. The conference had 7 keynote speakers from various universities in the US and India. The conference proceedings was published by IEEE-CS conference publication services (CPS). This event brought significant visibility to UNT. Professor Mohanty discussed graduate studies opportunities at UNT with students at the event.

In the alumni meeting of Government College of Engineering & Technology, Bhubaneswar (CETB), Professor Mohanty was honored as a distinguished alumnus. CETB is the constituent college and nerve center of the technical university of the state of Odisha called Biju Patanaik University of Technology (BPUT) which looks after all the engineering colleges of the state. A fully Government funded institute, CETB is located in the capital city Bhubaneswar of the Odisha state. CETB admits students who rank in the top 1% in the state level examination in which approximately 40,000 students participate every year. Prof. Mohanty discussed possible collaborations between UNT and this Institute. Prof. Mohanty also discussed possibilities of graduate studies at UNT with many students of CETB and invited them apply to UNT in their areas of interest.

In other news from NanoSystem Design Laboratory (NSDL), several more students have joined. NSDL now has 5 Ph.D. students and 4 M.S. thesis students engaged in various area of Nanoelectronics. For example, graduate student Shital Joshi comes from the Indian Institute of Technology (IIT) Varanasi, a leading institute of India. In the last year, members of NSDL published a total of 14 journal/conference articles. A selected example include the following:

- S. P. Mohanty and E. Kougianos, "Incorporating Manufacturing Process Variation Awareness in Fast Design Optimization of Nanoscale CMOS VCOs", IEEE Transactions on Semiconductor Manufacturing (TSM), Volume 27, Issue 1, February 2014, pp. 22-31.
- S. P. Mohanty and E. Kougianos, "Polynomial Metamodel Based Fast Optimization of Nano-CMOS Oscillator Circuits", Springer Analog Integrated Circuits and Signal Processing Journal, Volume 79, Issue 3, June 2014, pp. 437-453.
- S. P. Mohanty, M. Gomathisankaran, and E. Kougianos, "Variability-Aware Architecture Level Optimization Techniques for Robust Nanoscale Chip Design", Elsevier International Journal on Computers and Electrical Engineering (IJCEE), Volume 40, Issue 1, January 2014, pp. 168-193.

