UNIVERSITY OF NORTH★TEXAS™

COMPUTER SCIENCE & ENGINEERING

ALUMNI NEWSLETTER

Greetings from the CSE Chair

January 2015

CSE News
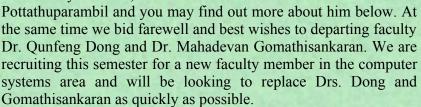**CSE Alumni News**
Student News
College of Engineering News
UNT News

Dear CSE Alumni and Friends,

At the beginning of our Spring 2015 semester, I want to share some news from our CSE Department. We welcome our new faculty member, Dr. Robin Pottathuparambil and you may find out more about him below. At the same time we bid farewell and best wishes to departing faculty Dr. Qunfeng Dong and Dr. Mahadevan Gomathisankaran. We are recruiting this semester for a new faculty member in the computer systems area and will be looking to replace Drs. Dong and Gomathisankaran as quickly as possible.

Our CSE Department is sponsoring several events this semester. Our Distinguished Speaker series will continue in Spring 2015. We will have four speakers this semester, including experts in domain-specific programming languages and computer security. We are also sponsoring a Workshop on Hot Topics in Networking and Security. Please read more information below on those events. Alumni are welcome to attend

In Alumni News, George Mobus is featured in our Alumni Focus below. Engineering Week is coming up and we invite alumni to come back to UNT to be a "Professor for a Day." It is always a great experience for our current students when our alumni lecture in our classes about their path to success. Contact me to find out how you can help support our CSE Department now!

Barrett Bryant
Professor and Chair

Department of Computer Science and

Professor Mohanty Chairs International Conference and Honored as a Distinguished Alumnus

Professor Mohanty welcoming the audience in the inaugural speech.

Professor Mohanty receives a distinguished alumni award from the Principal (i.e. Dean) of the Government College of Engineering & Technology, Bhubaneswar.

Professor Saraju Mohanty was a general chair for 13th International conference on information held in Bhubaneswar, India during December 22-24, 2014. The conference had 7 keynote speakers from various universities in the US and India. The conference proceedings was published by IEEE-CS conference publication services (CPS). This event brought significant visibility to UNT. Professor Mohanty discussed graduate studies opportunities at UNT with students at the event.

In the alumni meeting of Government College of Engineering & Technology, Bhubaneswar (CETB), Professor Mohanty was honored as a distinguished alumnus. CETB is the constituent college and nerve center of the technical university of the state of Odisha called Biju Patanaik University of Technology (BPUT) which looks after all the engineering

colleges of the state. A fully Government funded institute, CETB is located in the capital city Bhubaneswar of the Odisha state. CETB admits students who rank in the top 1% in the state level examination in which approximately 40,000 students participate every year. Prof. Mohanty discussed possible collaborations between UNT and this Institute. Prof. Mohanty also discussed possibilities of graduate studies at UNT with many students of CETB and invited them apply to UNT in their areas of interest.

In other news from NanoSystem Design Laboratory (NSDL), several more students have joined. NSDL now has 5 Ph.D. students and 4 M.S. thesis students engaged in various area of Nanoelectronics. For example, graduate student Shital Joshi comes from the Indian Institute of Technology (IIT) Varanasi, a leading institute of India. In the last year, members of NSDL published a total of 14 journal/conference articles. A selected example include the following:

- S. P. Mohanty and E. Kougianos, "Incorporating Manufacturing Process Variation Awareness in Fast Design Optimization of Nanoscale CMOS VCOs", IEEE Transactions on Semiconductor Manufacturing (TSM), Volume 27, Issue 1, February 2014, pp. 22-31.

- S. P. Mohanty and E. Kougianos, "Polynomial Metamodel Based Fast Optimization of Nano-CMOS Oscillator Circuits", Springer Analog Integrated Circuits and Signal Processing Journal, Volume 79, Issue 3, June 2014, pp. 437-453.

- S. P. Mohanty, M. Gomathisankaran, and E. Kougianos, "Variability-Aware Architecture Level Optimization Techniques for Robust Nanoscale Chip Design", Elsevier International Journal on Computers and Electrical Engineering (IJCEE), Volume 40, Issue 1, January 2014, pp. 168-193.