



Greetings from the CSE Chair



Dear CSE Alumni and Friends,

Our CSE Department is growing. The number of entering students has increased 40% from the same time last year. Of course, having more students require more faculty. Three new faculty members joined us in Fall 2012. Now we are searching for a faculty member in the security area. We will be interviewing candidates during the next few weeks. We hope to make a decision during March. Look for our announcement in the next CSE Alumni Newsletter.

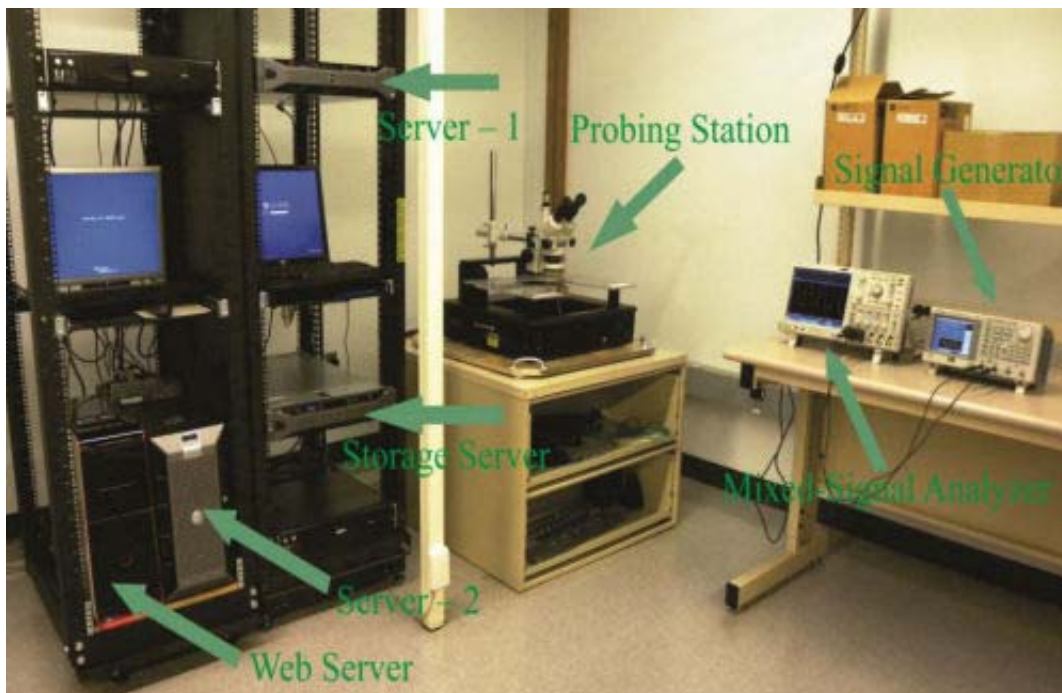
Congratulations again to Dr. Kavi and the Net-Centric IUCRC on winning the Tech Titan award. We just received the award check for \$20,000 at the end of January. This will be reinvested into the IUCRC. Congratulations to our CSE Cyber Defense Team on qualifying for the Southwest Regional Collegiate Cyber Defense Competition! Coming up in April, our CSE Department will host the SoMiC Workshop: Security on the Move and in the Clouds for the second year. Read below how you can make a presentation at this workshop. Please check out all the news below from our CSE research groups.

I am still excited from seeing so many alumni at our 40th Anniversary Celebration, but please don't wait another ten years to come back to UNT!

February 2013

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UNT News

News from NanoSystem Design Laboratory (NSDL)



The NanoSystem Design Laboratory (NSDL) had a very productive year of 2011-2012. During this time, 6 peer-reviewed journal papers were published and 16 conference publications were presented. A book was co-authored by Prof. Mohanty and additionally two patents were filled through UNT. NSDL has now 3 PhD, 2 masters, and 2 undergraduate students actively engaged in cutting edge research in nanoelectronics.

Selected journal and transaction publications which include student co-authors are the following:

- O. Garitselov, S. P. Mohanty, and E. Kougianos, "A Comparative Study of Metamodels for Fast and Accurate Simulation of Nano-CMOS Circuits", *IEEE Transactions on Semiconductor Manufacturing (TSM)*, Vol. 25, No. 1, February 2012, pp. 26-36.
- S. P. Mohanty, E. Kougianos, and O. Okobiah, "Optimal Design of a Dual-Oxide Nano-CMOS Universal Level Converter for Multi-Vdd SoCs", *Springer Analog Integrated Circuits and Signal Processing Journal*, Vol. 72, No. 2, August 2012, pp. 451-467.

NSDL PhD candidate, **Geng Zheng**, made two presentations at **GLSVLSI**

2012 in Salt Lake City, UT. In particular, the following paper is related to his PhD dissertation:

- G. Zheng, S. P. Mohanty, E. Kougianos, and O. Garitselov, "Verilog-AMS-PAM: Verilog-AMS integrated with Parasitic-Aware Metamodels for Ultra-Fast and Layout-Accurate Mixed-Signal Design Exploration", in *Proceedings of the 21st ACM/IEEE Great Lakes Symposium on VLSI (GLSVLSI)*, pp. 351--356, 2012.

Another NSDL PhD candidate **Karo Okobiah** made the following presentation at the International Symposium on Quality Electronic Design (**ISQED**) held at Santa Clara, CA:

- O. Okobiah, S. P. Mohanty, and E. Kougianos, "Ordinary Kriging Metamodel-Assisted Ant Colony Algorithm for Fast Analog Design Optimization", in *Proceedings of the 13th IEEE International Symposium on Quality Electronic Design (ISQED)*, pp. 458-463, 2012.

Both Geng and Karo also made presentations at IEEE Computer Society Annual Symposium on VLSI (**ISVLSI 2012**) which was held at Amherst, MA. Two papers were also presented at a VLSI Design Conference held in Hyderabad, India to which student members of NSDL traveled.

CSE Alumni News

Alumni Focus on Wes Fox

I graduated from UNT in 1992 with a BS in Computer Science and minor in Mathematics. Instead of listing my titles, honors and accomplishments (if you are interested in that, look me up on LinkedIn), I thought I would share some of my memories and things that were learned along the way. That being said, I have worked every role in a company from entry level tester to President & COO of a multimillion dollar corporation. The most important lesson I

